Abstract Hoare Logic

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Outline











Outline

Introduction

2 System Categories

3 Abstract Hoare Logic





Overview

What:

Abstraction of modular reasoning about 'while programs'



Overview

What:

Abstraction of modular reasoning about 'while programs'

• How:

Using system theory, tmc, and fixed-point theory



Overview

What:

Abstraction of modular reasoning about 'while programs'

• How:

Using system theory, tmc, and fixed-point theory

• Why:

Develop Hoare-logic for dynamical systems



Outline

Introduction



3 Abstract Hoare Logic























 $H \to (H \uplus H)$









 $(H \uplus H) \to H$







 $H \to (H \uplus H)$



Exploit the duality between sum and product

$$2^{H \uplus J} \simeq 2^H \times 2^J$$





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Monoidal Categories

• Sequential composition: categorical composition $f: X \to Y$, $g: Y \to Z$ then $g \circ f: X \to Z$

$$g \circ f \longrightarrow f \longrightarrow g$$

• **Parallel composition**: Monoidal operation $f: X \to Y, g: Z \to W$ then $f \otimes g: (X \otimes Z) \to (Y \otimes W)$





Abstract Hoare Logic

Traced Monoidal Categories

• Iteration: Trace operation

If $f:(X\otimes Z)\to (Y\otimes Z)$ then ${\rm Tr}(f):X\to Y$







Abstract Hoare Logic

Traced Monoidal Categories

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• Examples

• Disjoint union $\mathsf{Tr}(f) \equiv \{ \langle x, y \rangle : \exists z_0, \dots, z_n(\langle x, z_0 \rangle \in f \land \dots \land \langle z_n, y \rangle \in f) \}$

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• Cartesian products $\mathsf{Tr}(f) \equiv \{ \langle x, y \rangle : \exists z (\langle \langle x, z \rangle, \langle y, z \rangle \rangle \in f) \}$

System Category

Let ${\rm cl}(M)$ denote the closure of the set of morphisms M under sequential and monoidal composition, and trace.

Definition (System category)

A system category S is a traced monoidal category with a distinguished set of morphisms $S_b \subseteq S_m$, so-called *basic systems*, such that $cl(S_b) = S_m$.



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| Flowcharts | Stream circuits |
|--|--|
| / | |
| Boolean Test $(\Sigma 	o \Sigma \uplus \Sigma)$ | $Sum\ (\Sigma \times \Sigma \to \Sigma)$ |
| Joining of Wires $(\Sigma \uplus \Sigma \to \Sigma)$ | Splitting of Wires ($\Sigma \to \Sigma \times \Sigma$) |
| Assignment ($\Sigma \rightarrow \Sigma$) | Scalar Multiplication ($\Sigma 	o \Sigma$) |
| | Register ($\Sigma \to \Sigma$) |

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• *Pre/Post-conditions*: Describe properties of input/output



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• Ordering on information: Rule of consequence



- Pre/Post-conditions: Describe properties of input/output
- Ordering on information: Rule of consequence
- Partial correctness assertions: Predicate transformers



- Pre/Post-conditions: Describe properties of input/output
- Ordering on information: Rule of consequence
- Partial correctness assertions: Predicate transformers
- Others:

Strongest post condition, loop invariant, ...

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Verification Category

Let Pos denote the category of posets and monotone mappings

Definition (Verification category)

A subcategory \mathcal{V} of Pos is called a *verification category* if for any element $P \in X$ and morphism $f : (X \times Z) \to (Y \times Z)$ the set of pre-fixed points, i.e.

 $\{Q : \exists R. f \langle P, Q \rangle \sqsubseteq \langle R, Q \rangle \}$

has a least element. We will denote such least element by $\mu_{f,P}$.



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By monotonicity of f, $\mu_{f,P}$ is also the least fixed point.

Verification Category: Intuition

-

| Usual Hoare Logic | Verification Categories |
|--------------------------|-------------------------|
| | |
| Pre/Post-conditions | Points of posets |
| Logical implication | Partial order |
| Rule of consequence | Monotonicity |
| Strongest loop invariant | Least pre-fixed point |



Verification Category and TMC

Lemma (A)

Any verification category ${\mathcal V}$ gives rise to a traced monoidal category with trace defined as

 ${\rm Tr}(f)(P):\equiv R$

for any morphism $f : (X \times Z) \to (Y \times Z)$, where R is the unique element of Y such that $f\langle P, \mu_{f,P} \rangle = \langle R, \mu_{f,P} \rangle$.





Propagation of Upper Bounds

Theorem (Soundness and completeness)

Let \mathcal{V} be a verification category and \mathcal{V}_b a set of basic morphisms spanning \mathcal{V}_m . The following set of propagation of upper bound rules is sound and complete for \mathcal{V} with respect to \mathcal{V}_b

$$\frac{f \in \mathcal{V}_b}{f(P) \sqsubseteq f(P)} \text{ (axiom)}$$

$$\frac{P'\sqsubseteq P \quad f(P)\sqsubseteq Q \quad Q\sqsubseteq Q'}{f(P')\sqsubseteq Q'} \ ({\rm con})$$



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$$\frac{f \in \mathcal{V}_{b}}{f(P) \sqsubseteq f(P)} (\operatorname{axiom}) \qquad \frac{f(P) \sqsubseteq Q \quad g(Q) \sqsubseteq R}{(g \circ f)(P) \sqsubseteq R} (\circ)$$
$$\frac{f(P) \sqsubseteq Q \quad g(R) \sqsubseteq S}{(f \times g) \langle P, R \rangle \sqsubseteq \langle Q, S \rangle} (\times)$$
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$$\frac{P' \sqsubseteq P \quad f(P) \sqsubseteq Q \quad Q \sqsubseteq Q'}{f(P') \sqsubseteq Q'} (\operatorname{con})$$

















Definition (Verification functor)

A monoidal functor $H: \mathcal{S} \to \mathsf{Pos}$ is called a *verification functor* if

- image of H is a verification category
- H preserves traces (trace in image of H defined in Lemma (A))



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Let

- $H: \mathcal{S} \to \mathsf{Pos}$ be a verification functor
- $f: X \to Y$ is a morphism (system) in \mathcal{S}
- $P \in H(X)$ and $Q \in H(Y)$

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We define abstract Hoare triples as

$$\{P\} \ f \ \{Q\} \ :\equiv \ H(f)(P) \sqsubseteq_{H(Y)} Q$$

Abstract Hoare Logic

Theorem (Soundness and completeness)

The following set of rules is sound and complete for any system category S and verification functor $H : S \rightarrow Pos$:

$$\frac{f \in \mathcal{S}_{b}}{\{P\} f \{H(f)(P)\}} (\operatorname{axiom}) \quad \frac{\{P\} f \{Q\} \quad \{Q\} g \{R\}}{\{P\} g \circ f \{R\}} (\circ)$$

$$\frac{\{P\} f \{Q\} \quad \{R\} g \{S\}}{\{\langle P, R \rangle\} f \otimes g \{\langle Q, S \rangle\}} (\otimes) \quad \frac{\{\langle P, Q \rangle\} f \{\langle R, Q \rangle\}}{\{P\} \operatorname{Tr}_{\mathcal{S}}(f) \{R\}} (\operatorname{Tr}_{\mathcal{S}})$$

$$\frac{P' \sqsubseteq_{X} P \quad \{P\} f \{Q\} \quad Q \sqsubseteq_{Y} Q'}{\{P'\} f \{Q'\}} (\operatorname{wkn})$$



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Flowcharts

The embedding H is basically the power-set construction, so that $H(X \uplus Y) :\equiv H(X) \times H(Y)$

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On morphisms, we define:

Forward reasoning

 $H(f)(P) :\equiv \{y \in Y \ : \ \exists x \! \in \! P \ (f(x) = y)\}$

Backward reasoning

 $H(f)(Q) :\equiv \{x \in X : f(x) \in Q\}$

And if sets are described by formulas:

- $H(f)(\Phi) :\equiv \mathsf{SPC}(f, \Phi)$
- $H(f)(\Phi) := \mathsf{WPC}(f, \Phi)$

Abstract Hoare Logic

While Loop Rule

while b(C)

 $(1 \uplus C) \circ \mathsf{if}_b \circ \Delta$







 $\mathsf{while}_b(C)$



$$\mathsf{Tr}((1 \uplus C) \circ \mathsf{if}_b \circ \Delta)$$





 $\mathsf{while}_b(C)$

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$$\frac{ \{I\} \text{ if}_{b} \{\langle I \land \neg b, I \land b \rangle\}}{\{I \land \neg b, I \land b \rangle\}} \frac{ \{I \land \neg b\} \ 1 \{I \land \neg b\} \ \{I \land b\} \ C \{I\}}{\{\langle I \land \neg b, I \land b \rangle\}} \stackrel{(\texttt{t})}{\texttt{t} \uplus C \{\langle I \land \neg b, I \rangle\}}}{(\texttt{t})} \frac{ \{I\} \ (1 \uplus C) \circ \text{if}_{b} \{\langle I \land \neg b, I \rangle\}}{\{\langle I, I \rangle\} \ (1 \uplus C) \circ \text{if}_{b} \circ \Delta \{\langle I \land \neg b, I \rangle\}} \stackrel{(\circ)}{\texttt{t}}{\texttt{t} \lor \texttt{t} \lor \texttt{t}} \frac{ \{I\} \ \texttt{t} \lor \texttt{t$$

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Abstract Hoare Logic

Instantiations

While Loop Rule

while_b(C)

 $\mathsf{Tr}((1 \uplus C) \circ \mathsf{if}_b \circ \Delta)$

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$$\frac{\{I\} \text{ if}_{b} \left\{ \langle I \land \neg b, I \land b \rangle \right\}}{\{I \land \neg b\} 1 \left\{I \land \neg b\right\} 1 \left\{I \land b\right\} C \left\{I\right\}} \frac{\{I\} \text{ if}_{b} \left\{ \langle I \land \neg b, I \land b \rangle \right\}}{\{\langle I \land \neg b, I \land b \rangle\} 1 \uplus C \left\{ \langle I \land \neg b, I \rangle \right\}} (\uplus)}{\{I\} (1 \uplus C) \circ \text{ if}_{b} \left\{ \langle I \land \neg b, I \rangle \right\}} \frac{\{I\} (1 \boxminus C) \circ \text{ if}_{b} \circ \Delta \left\{ \langle I \land \neg b, I \rangle \right\}}{\{\langle I, I \rangle\} (1 \uplus C) \circ \text{ if}_{b} \circ \Delta \left\{ \langle I \land \neg b, I \rangle \right\}} (\circ)} \frac{\{I\} \text{ while}_{b}(C) \{I \land \neg b\}}{\{I\} \text{ while}_{b}(C) \{I \land \neg b\}} (\intercal)}$$



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while_b(C) $Tr((1 \uplus C) \circ if_b \circ \Delta)$ $I \land \neg b \land I \land \neg b \land \neg \partial \neg \neg \neg b \land \neg \partial \neg \partial \neg \neg \neg \neg (\neg$

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Smooth functions can be represented as streams

$$\sigma_y = [y(0), y'(0), y''(0), \dots]$$

Stream circuits basic operations:





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Stream circuits basic operations:



Embedding

- Stream circuits already have Cartesian product as the monoidal structure, the embedding *H* into Pos has to respect that
- Our verification embedding of stream circuits is as follows:
 - Define $H(\Sigma)$ as the poset of finite approximations (prefixes) of elements in Σ , with the ordering $s \preceq t$, if t is an extension of s

• For morphisms (stream circuits) $f: X \to Y$ define $H(f)(t) :\equiv \{f(t * \tau) : \tau \in X\}$

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Conclusions and Future Work

- Other instantiations (in flowcharts):
 - Pointer programs
 - Total correctness
- Other instantiations (in stream circuits):
 - Boundedness
 - Relative stability
- Related work
 - Dijkstra's predicate transformer
 - Kozen's KAT (Kleene Algebras with Test)

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- Abramsky's specification categories
- Bloom and Esik on iteration theory
- Gurevich's existential fixed-point logic